

L Number	Hits	Search Text	DB	Time stamp
8	16	(((((711/103).CCLS.) and @ad < "20010830") and flash) and compression) and decompress\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 13:59
-	1057	(711/103).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:17
-	600	(((((711/103).CCLS.) and @ad < "20010830") and flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:57
-	50	(((((711/103).CCLS.) and @ad < "20010830") and flash) and compression	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:57
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous near2 RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:12
-	3	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous with RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:15
-	4	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and ((synchronous with RAM) sdram sldram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:13
-	97	(flash with memory with compress\$4) and ((power adj up) boot) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:59
-	75	(flash with memory with decompress\$4) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:12
-	2	"20030046483"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:13
-	2	"20030046486"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:14
-	2	"6549468"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:15
-	7	"6560161"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:20
-	2	"20030023840"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:20

-	2209	((flash with memory) and (power adj up) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 13:38
-	36	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:30
-	34	((flash with memory) with (RAM) with (load read)) same ((power adj up) reset)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:46
-	184	((flash with memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:47
-	152	((flash near4 memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 16:13
-	2	"20020138702"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 16:18
-	2	"20020016888"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:22
-	2	"20020016888" and (exclusive adj lock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:23
-	2	"20030046485"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:16
-	3	((system adj reset) with (cpu near4 activa\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:17
-	338	((CPU processor system) adj reset) with (boot)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:22
-	232	((CPU processor system) adj reset) with (boot)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:18
-	14	((CPU processor system) adj reset) near signal) with (boot)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:32
-	3	((CPU processor system) adj reset) with (boot near5 (preload (pre adj load))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:33

-	15	((boot near5 load) near (complet\$4 ack acknowledg\$5)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:41
-	0	((boot near5 load) near (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:42
-	9	((boot near5 load) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:43
-	2	((boot near5 load\$4) near4 (complet\$4 after)) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:27
-	65	(flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:28
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and RDRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:28
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with DRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:29
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:29
-	2566	(Rambus with (RAM dram rdram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:31
-	4	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)and rambus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:30
-	1504	((Rambus with (RAM dram rdram))) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:33
-	830	((711/103).CCLS.) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:33
-	1	((711/103).CCLS.) and @ad < "20010830") and rdram	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:34
-	3	((711/103).CCLS.) and @ad < "20010830") and rambus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:34

-	2	("20030046485").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 18:22
-	0	((flash with card with compress\$4) and processor and @ad< "20010830") and (synchronous with RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:15
-	0	((flash with decompress\$4) and processor and @ad< "20010830") and (synchronous with RAM) and (flash with (card pcmcia))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:17
-	0	((flash same decompress\$4)and @ad< "20010830") and (synchronous with RAM) and (flash with (card pcmcia))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:18
-	108	@ad< "20010830" and (synchronous with RAM) and (flash with (card pcmcia))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:22
-	200	@ad< "20010830" and (sdram) and (flash with (card pcmcia))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:27
-	200	( @ad< "20010830" and (sdram) and (flash with (card pcmcia))) and (flash decompress\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:22
-	16	( @ad< "20010830" and (sdram) and (flash with (card pcmcia))) and (flash with decompress\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:23
-	23	@ad< "20010830" and (sdram) and (flash with pcmcia)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:30
-	6	@ad< "20010830" and (sdram) and ((flash with remov\$4) same pcmcia)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/30 20:31